

Fig. 1

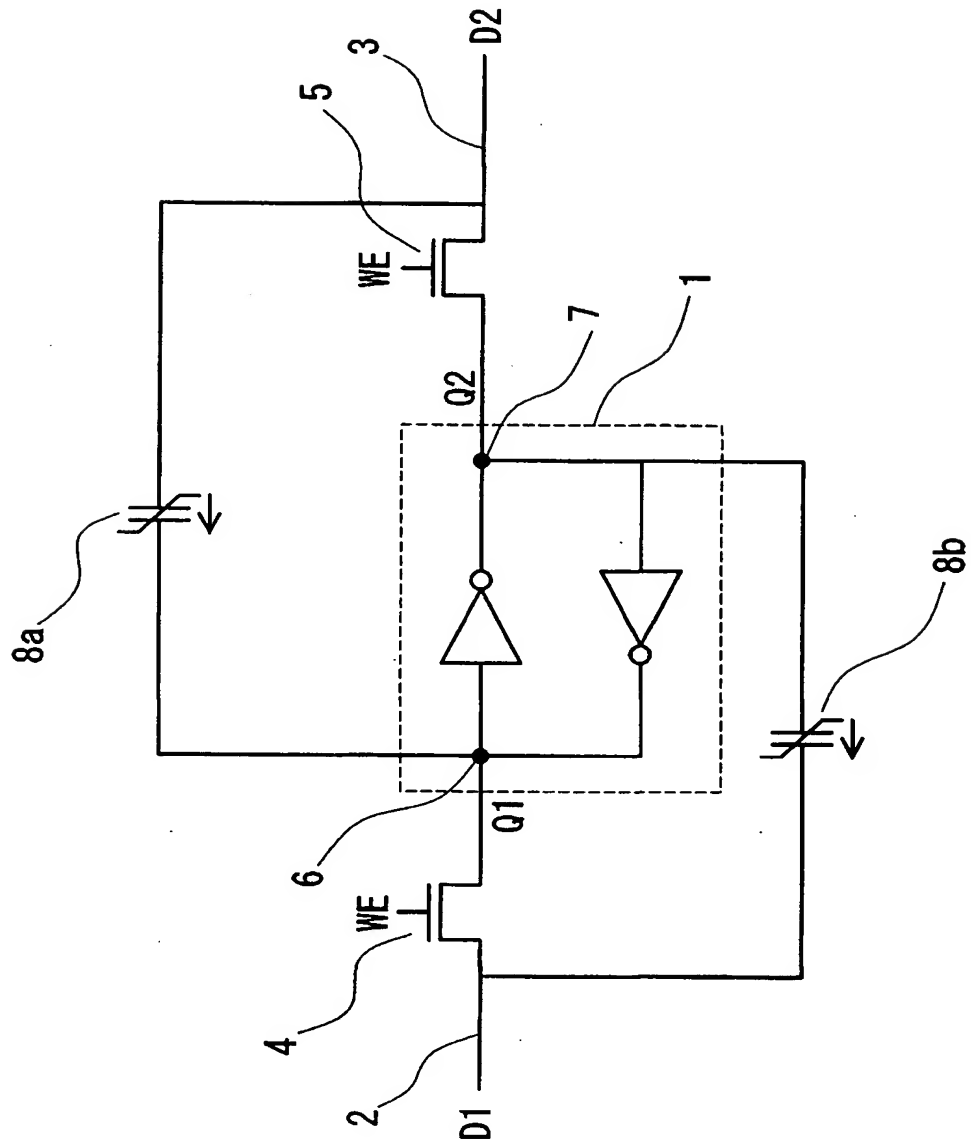


Fig. 2

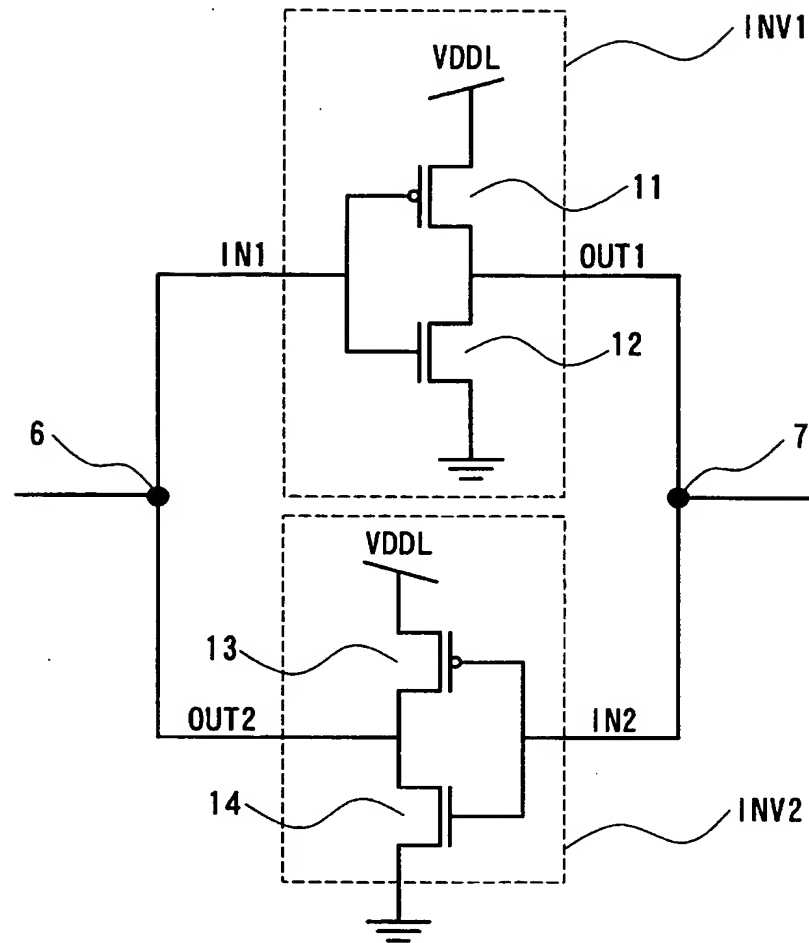


Fig. 3

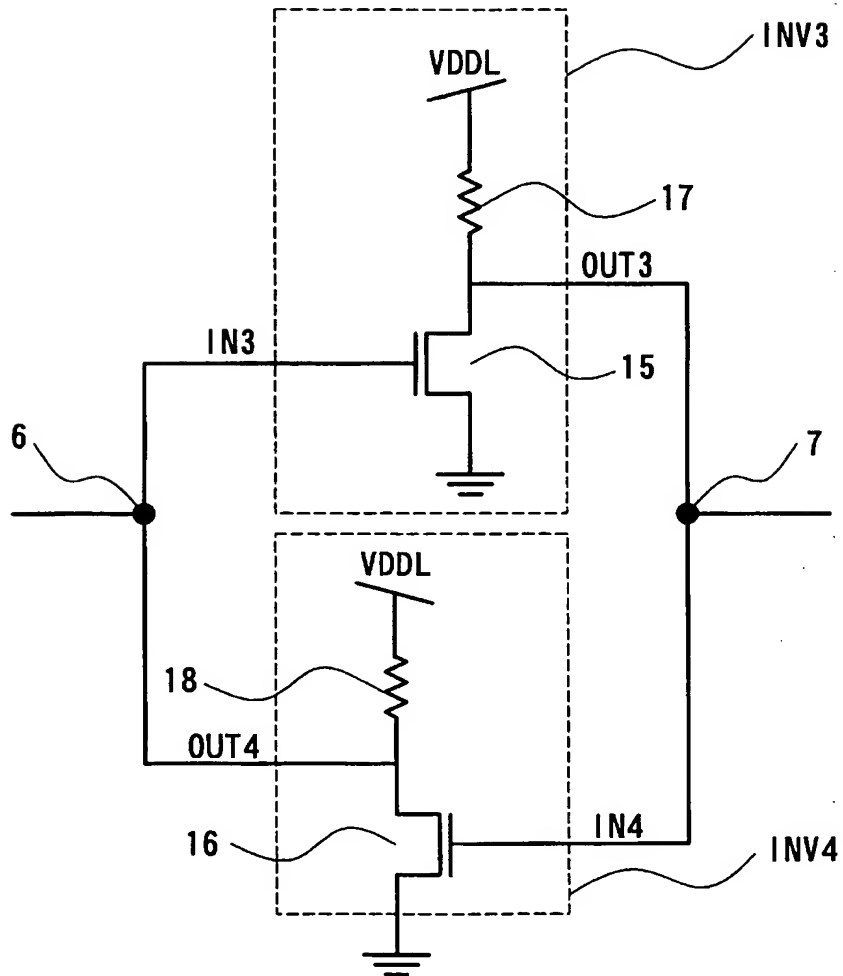


Fig. 4

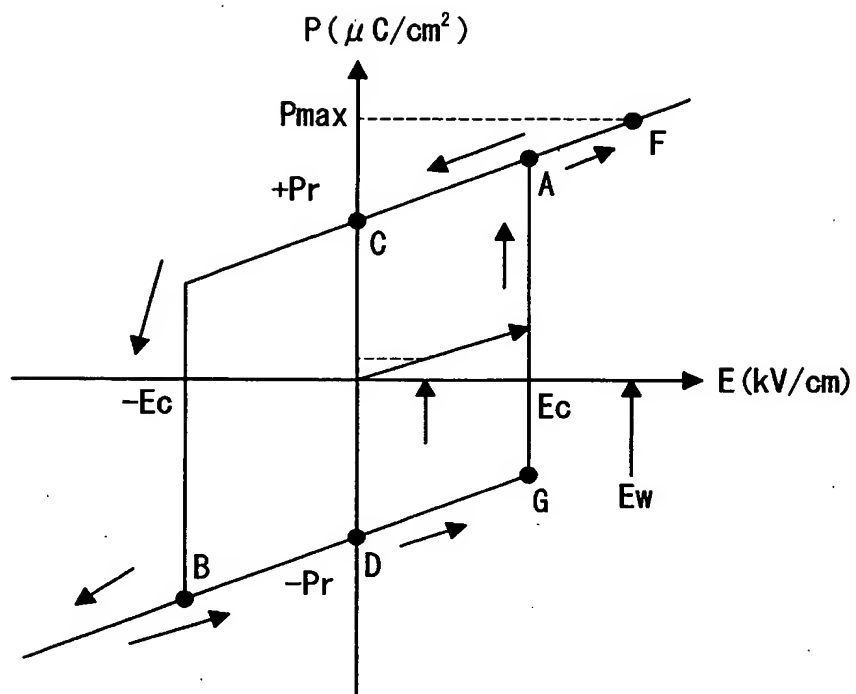


Fig. 5

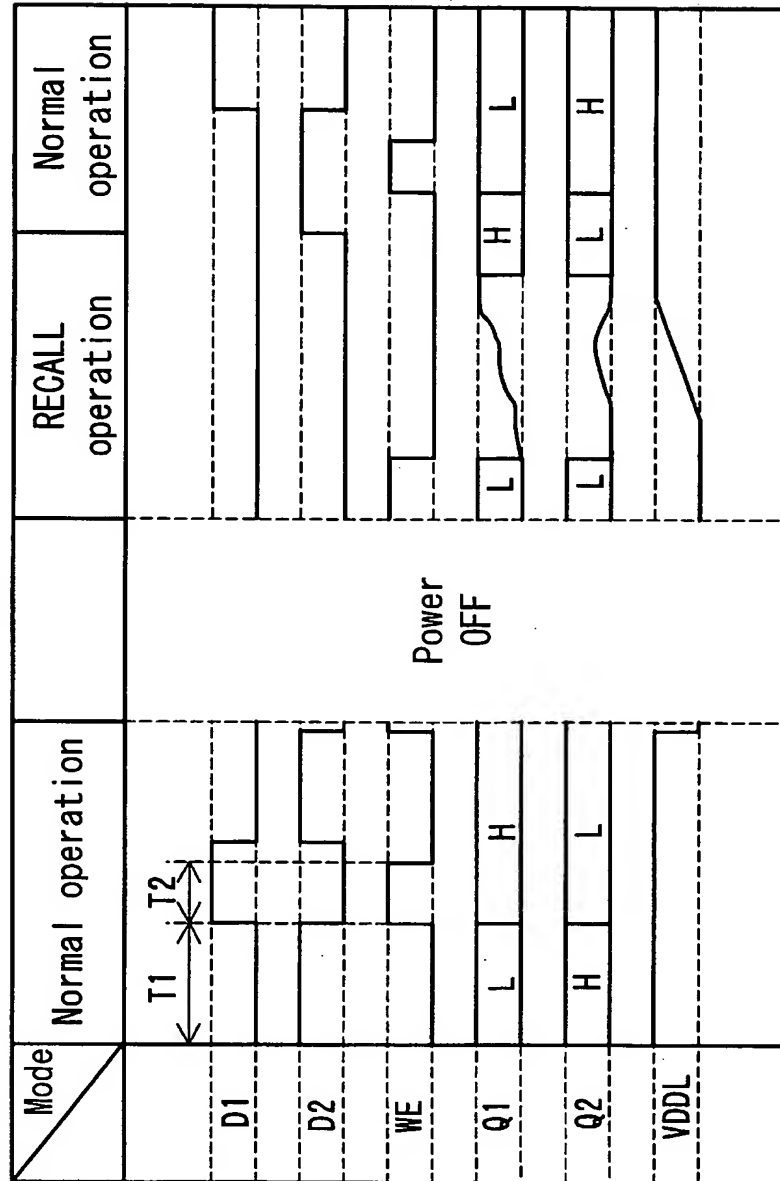


Fig. 6

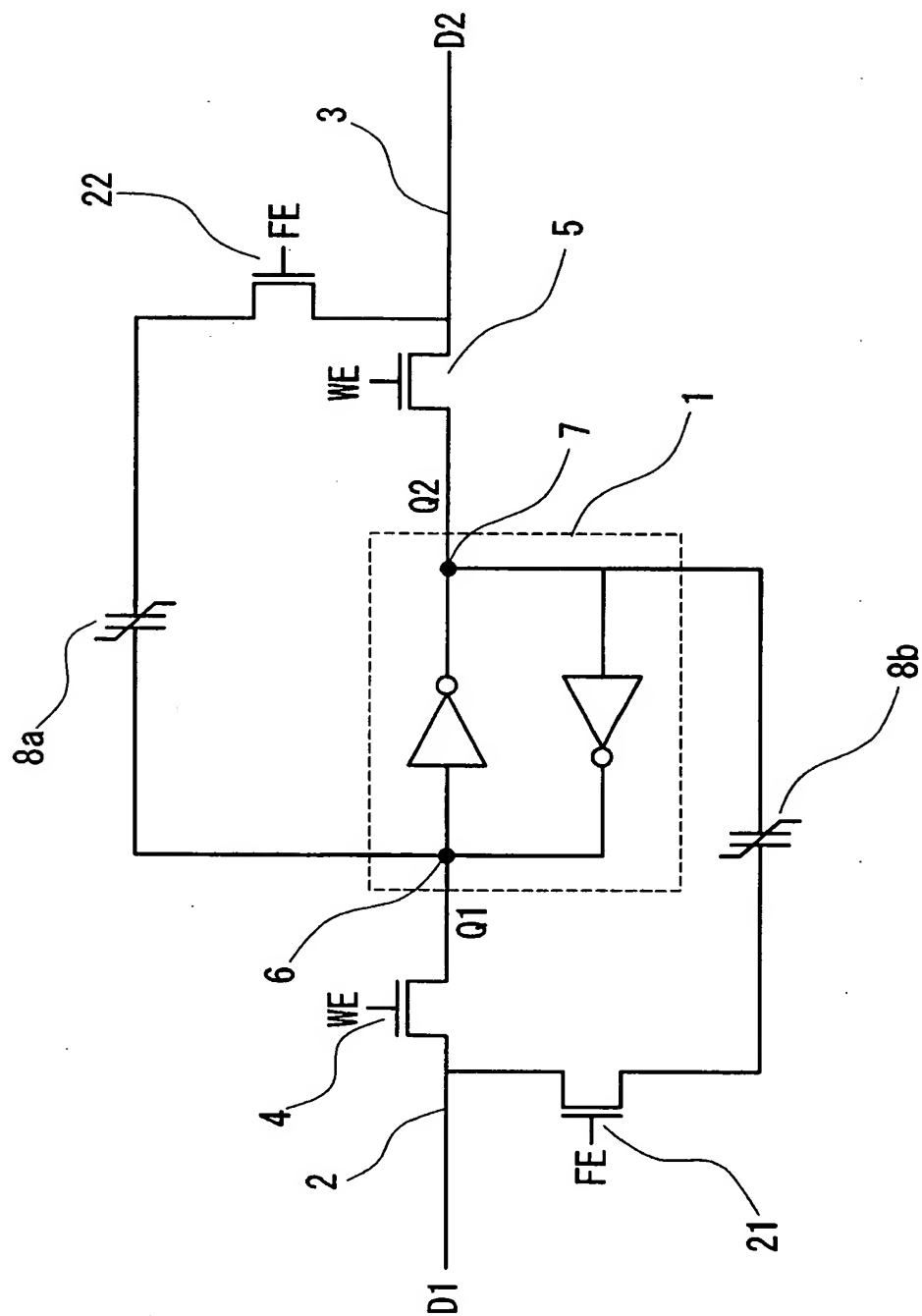


Fig. 7





























Mode	Normal operation	STORE operation		RECALL operation	Normal operation
D1			Power OFF		
D2					
WE					
FE					
Q1					
Q2					
VDDL					

Fig. 8

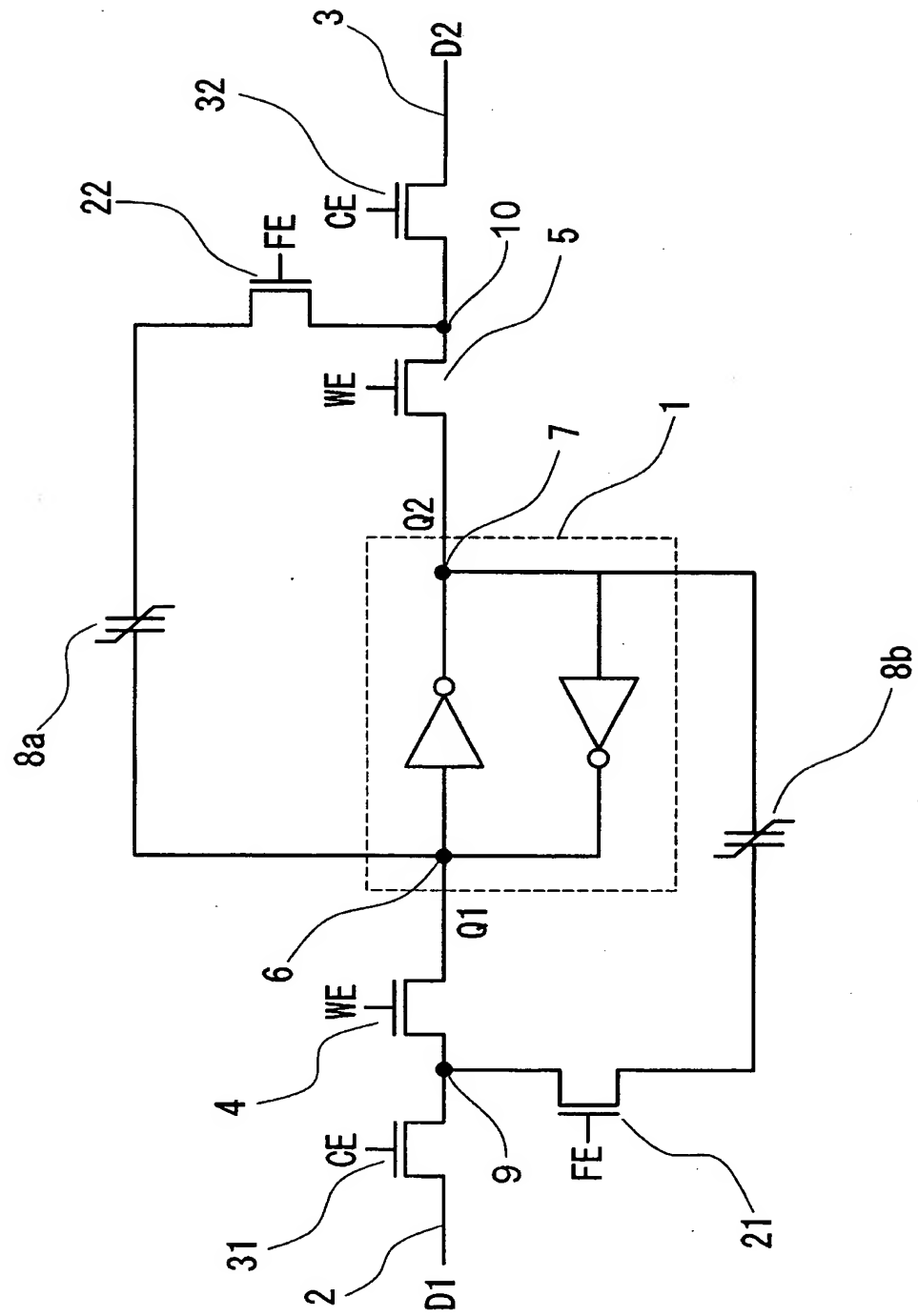


Fig. 9








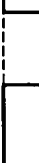



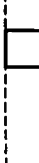













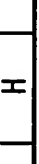




Mode	Normal operation	STORE operation		RECALL operation	Normal operation
D1			Power OFF		
D2					
CE					
WE					
FE					
Q1	Uncertain H L L H L				
Q2	Uncertain L L H L L				
VDDL					

Fig. 10

PRIOR ART

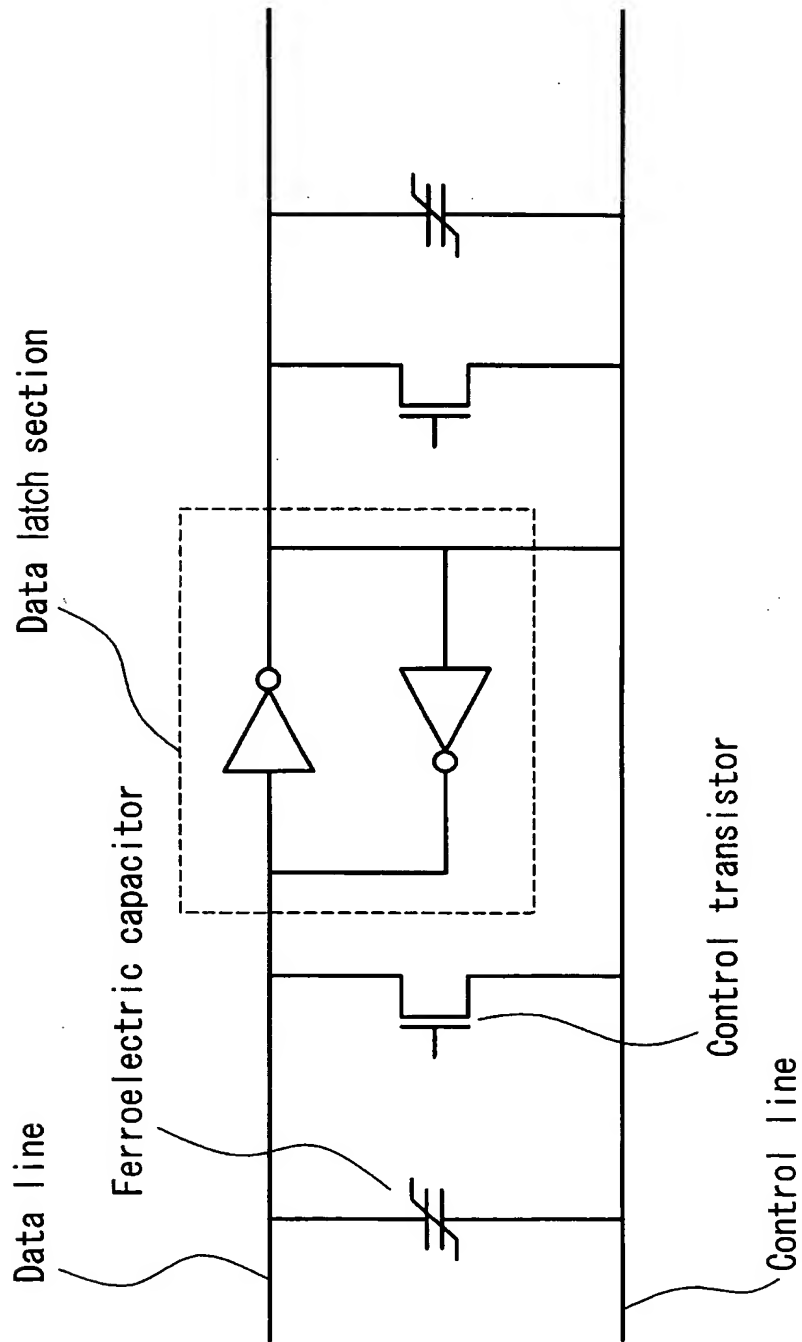


Fig. 11

PRIOR ART

